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24309 7590 12/22/2005

XILINX, INC
ATTN: LEGAL DEPARTMENT
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SAN JOSE, CA 95124

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Julie Matthews	(Depositor's name)
<i>Julie Matthews</i>	(Signature)
March 16, 2006	(Date)

03/17/2006 TBESHAH2 00000065 240040 10612522

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/612,522	07/02/2003	Cameron D. Patterson	X-1405 US	3307

TITLE OF INVENTION: FIELD PROGRAMMABLE GATE ARRAY (FPGA) CONFIGURATION DATA PATH FOR MODULE COMMUNICATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	03/22/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
ELMORE, STEPHEN C	2185	711-165000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list
(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 E. Eric Hoffman
2 Justin Liu
3

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC., 2100 Logic Drive, San Jose, CA 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee
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☐ Advance Order - # of Copies

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- ☐ A check in the amount of the fee(s) is enclosed.
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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above. NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent, or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature

Typed or printed name Justin Liu

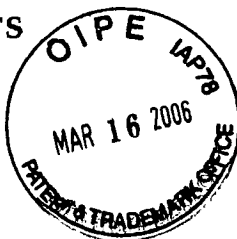
Date March 16, 2006

Registration No. 51,959

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Inventor(s): Cameron D. Patterson

Assignee: **XILINX, INC.**

Serial No.: 10/612,522

conf.no. 5307

Filed: July 2, 2003

Title: Field Programmable Gate Array (FPGA) Configuration Data Path for Module Communication

Docket No.: X-1405 US

Enclosed: Return Receipt Postcard
Part B - Fee(s) Transmittal Form

Date: March 16, 2006

VIA FACSIMILE 571-273-2885

Atty/Sec: JL/jam